



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yang, et al. Docket No.: TSM03-0929
Serial No.: 10/823,158 Art Unit: 2814
Filed: April 13, 2004 Examiner: Theresa T. Doan
For: Methods and Structures for Planar and Multiple-Gate Transistors Formed on SOI

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Amendment

Dear Sir:

Applicants respectfully submit the following amendments and remarks in response to Examiner's Office Action dated December 29, 2005. They are believed to be a full and complete response to said Action.